

10/555072

Docket No.: 034299-673

JC06 Rec'd PCT/PTO 28 OCT 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Pierre-Jean Ribeyron, et al.
SERIAL NO.: unassigned
FILING DATE: herewith
TITLE: Method for Production of a Semiconductor Device with Auto-
Aligned Metallisations
EXAMINER: UNASSIGNED
ART UNIT: UNASSIGNED

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Prior to examination, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks / Arguments begin on page 3 of this paper.